

# Freeform Search

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Term:	18 and 17
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Display:	20	Documents in Display Format:	-	Starting with Number	1
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Generate:	<input type="radio"/> Hit List	<input checked="" type="radio"/> Hit Count	<input type="radio"/> Side by Side	<input type="radio"/> Image
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Search	Clear	Interrupt
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## Search History

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<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
<i>DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L9</u>	18 and 17	5	<u>L9</u>
<u>L8</u>	14 near3 13	336	<u>L8</u>
<u>L7</u>	11 near3 L6	26	<u>L7</u>
<u>L6</u>	dirty	49936	<u>L6</u>
<u>L5</u>	head or beginning or first	7067646	<u>L5</u>
<u>L4</u>	tail or end or last or rear	7402508	<u>L4</u>
<u>L3</u>	lru or (least adj recently adj used)	7875	<u>L3</u>
<u>L2</u>	writ\$ near3 disk	65627	<u>L2</u>
<u>L1</u>	destag\$	880	<u>L1</u>

END OF SEARCH HISTORY

Results for "(destag\* and cache&lt;in&gt;metadata)"

Your search matched **23** of **1328352** documents.

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Modify Search


☐ Check to search only within this results set

Display Format: ☒ Citation ☐ Citation & Abstract

**» Key**

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEEE Conference Proceeding

IEEE STD IEEE Standard

[Select All](#) [Deselect All](#)

- ☐ **1. RAPID-Cache-a reliable and inexpensive write cache for disk I/O systems**  
Yiming Hu; Qing Yang; Nightingale, T.;  
High-Performance Computer Architecture, 1999. Proceedings. Fifth International Symposium On  
9-13 Jan. 1999 Page(s):204 - 213  
Digital Object Identifier 10.1109/HPCA.1999.744364  
[AbstractPlus](#) | Full Text: [PDF\(336 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ **2. Exploiting client caches: an approach to building large Web caches**  
Zhu, Y.; Hu, Y.;  
Parallel Processing, 2003. Proceedings. 2003 International Conference on  
2003 Page(s):419 - 426  
Digital Object Identifier 10.1109/ICPP.2003.1240606  
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- ☐ **3. Two-level cache for distributed system in RAID 5 disk controller**  
Jung-ho Huh;  
Systems Engineering, 2005. ICSEng 2005. 18th International Conference on  
16-18 Aug. 2005 Page(s):64 - 69  
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- ☐ **4. BUCS - a bottom-up cache structure for networked storage servers**  
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15-18 Aug. 2004 Page(s):310 - 317 vol.1  
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[AbstractPlus](#) | Full Text: [PDF\(445 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ **5. The design and implementation of RAPID-cache for Linux**  
Xu, Z.; Hu, Y.;  
Performance, Computing, and Communications Conference, 2005. IPCCC 2005. 24th IEEE International  
7-9 April 2005 Page(s):329 - 333  
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[AbstractPlus](#) | Full Text: [PDF\(317 KB\)](#) IEEE CNF  
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- ☐ **6. Simulation study of cached RAID5 designs**  
Treiber, R.; Menon, J.;

High-Performance Computer Architecture, 1995. Proceedings. First IEEE Symposium on  
22-25 Jan. 1995 Page(s):186 - 197

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**7. An adaptive high-low water mark destage algorithm for cached RAID5**

Young Jin Nam; Chanik Park;

Dependable Computing, 2002. Proceedings. 2002 Pacific Rim International Symposium on  
16-18 Dec. 2002 Page(s):177 - 184

Digital Object Identifier 10.1109/PRDC.2002.1185636

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**8. Design and performance evaluation of an adaptive parity cache technique**

Jungho Huh;

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**9. Improving hash join performance through prefetching**

Chen, S.; Ailamaki, A.; Gibbons, P.B.; Mowry, T.C.;

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30 March-2 April 2004 Page(s):116 - 127

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**10. A log-based write-back mechanism for cooperative caching**

Narasimhan, S.; Sohoni, S.; Yiming Hu;

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22-26 April 2003 Page(s):10 pp.

Digital Object Identifier 10.1109/IPDPS.2003.1213157

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**11. A caching strategy to improve iSCSI performance**

Xubin He; Qing Yang; Ming Zhang;

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Xubin He; Qing Yang; Ming Zhang;

Parallel Processing, 2002. Proceedings. International Conference on  
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Digital Object Identifier 10.1109/ICPP.2002.1040875

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**13. RAID5 performance with distributed sparing**

Thomasian, A.; Jai Menon;

Parallel and Distributed Systems. IEEE Transactions on  
Volume 8, Issue 6, June 1997 Page(s):640 - 657

Digital Object Identifier 10.1109/71.595583

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**14. Destage algorithms for disk arrays with non-volatile caches**

Varma, A.; Jacobson, Q.;

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- ☐ **15. Priority queueing in RAID disk arrays with an NVS cache**  
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[Modeling, Analysis, and Simulation of Computer and Telecommunication Systems, 1995. MASCOTS '95. Proceedings of the Third International Workshop on](#)  
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- ☐ **16. Destage algorithms for disk arrays with nonvolatile caches**  
Varma, A.; Jacobson, Q.;  
[Computers, IEEE Transactions on](#)  
Volume 47, Issue 2, Feb. 1998 Page(s):228 - 235  
Digital Object Identifier 10.1109/12.663770  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(160 KB\)](#) IEEE JNL  
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- ☐ **17. A new destage algorithm for disk cache: DOME**  
Alonso, M.; Santonja, V.;  
[EUROMICRO Conference, 1999. Proceedings, 25th](#)  
Volume 1, 8-10 Sept. 1999 Page(s):416 - 423 vol.1  
Digital Object Identifier 10.1109/EURMIC.1999.794503  
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- ☐ **18. An integrated performance model of disk arrays**  
Varki, E.; Merchant, A.; Xu, J.; Qiu, X.;  
[Modeling, Analysis and Simulation of Computer Telecommunications Systems, 2003. MASCOTS 2003. 11th IEEE/ACM International Symposium on](#)  
12-15 Oct. 2003 Page(s):296 - 305  
Digital Object Identifier 10.1109/MASCOT.2003.1240674  
[AbstractPlus](#) | Full Text: [PDF\(450 KB\)](#) IEEE CNF  
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- ☐ **19. Issues and challenges in the performance analysis of real disk arrays**  
Varki, E.; Merchant, A.; Xu, J.; Qiu, X.;  
[Parallel and Distributed Systems, IEEE Transactions on](#)  
Volume 15, Issue 6, June 2004 Page(s):559 - 574  
Digital Object Identifier 10.1109/TPDS.2004.9  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(1672 KB\)](#) IEEE JNL  
[Rights and Permissions](#)
  
- ☐ **20. UCFS - a novel User-space, high performance, Customized File System for Web proxy servers**  
Jun Wang; Rui Min; Yingwu Zhu; Yiming Hu;  
[Computers, IEEE Transactions on](#)  
Volume 51, Issue 9, Sept. 2002 Page(s):1056 - 1073  
Digital Object Identifier 10.1109/TC.2002.1032625  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(2236 KB\)](#) IEEE JNL  
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- ☐ **21. The design and performance evaluation of the RAID 5 controller using the load-balanced destage algorithm**  
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- ☐ **22. Towards an object store**  
Azagury, A.; Dreizin, V.; Factor, M.; Henis, E.; Naor, D.; Rinetzky, N.; Rodeh, O.; Satran, J.;

Tavory, A.; Yerushalmi, L.;

Mass Storage Systems and Technologies, 2003. (MSST 2003). Proceedings. 20th IEEE/11th NASA Goddard Conference on

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**23. Using MEMS-Based Storage to Boost Disk Performance**

Feng Wang; Bo Hong; Brandt, S.A.; Long, D.D.E.;

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Results

#1	((destag* and dirty)<in>metadata)	2
#2	(destag* and cache<IN>metadata)	23
#3	((destag* and cache<in>metadata)<AND>(destag* and cache<in>metadata) and lru)	13
#4	((destag* and cache<in>metadata)<AND>(destag* and cache<in>metadata) and lru)	13
#5	(destag* and cache<IN>metadata)	23